

### **High Voltage Switching Diode**

#### **General Description**

Dual general-purpose switching diodes, fabricated in planar technology, and packaged in small SOT-23 surface mounted device (SMD) packages.

#### **Features and Benefits**

- Silicon epitaxial planar diode
- High switching speed
- · Low forward drop voltage and low leakage current
- "Green" device and RoHS compliant device
- Available in full lead (Pb)-free device

#### **Applications**

• Ultra high speed switching application

#### **Ordering Information**

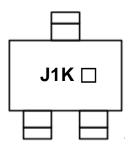






Part Number	Marking Code	Package	Packaging
SDS19WK	J1K 🗆	SOT-23	Tape & Reel

#### **Marking Information**



J1K = Specific Device Code

□ = Year & Week Code Marking

#### **Pinning Information**

Pin	Description	Simplified Outline	Graphic Symbol
1	Anode (Diode 1)	3	<u>_</u>
2	Anode (Diode 2)		× ×
3	Common Cathode	1 🗄 🗄 2	

#### Absolute Maximum Ratings (T<sub>amb</sub>=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Maximum repetitive peak reverse voltage	V <sub>RM</sub>	120	V
Continuous reverse voltage	V <sub>R</sub>	100	V
Maximum average forward rectified current	Ι <sub>ο</sub>	200	mA
Maximum repetitive peak forward current	I <sub>FM</sub>	400	mA
Non-repetitive peak forward surge current(t=10ms)	I <sub>FSM</sub>	1.7	А
Power dissipation <sup>1)</sup>	P <sub>D</sub>	250	mW

<sup>1)</sup> Device mounted on FR-4 board with recommended pad layout.

#### **Thermal Characteristics** (T<sub>amb</sub>=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Thermal resistance, junction to ambient <sup>1)</sup>	R <sub>th(j-a)</sub>	500	°C/W
Operating junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55 ~ 150	°C

<sup>1)</sup> Device mounted on FR-4 board with recommended pad layout.

#### Electrical Characteristics (T<sub>amb</sub>=25°C, Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Reverse breakdown voltage	$V_{BR}$	I <sub>F</sub> =100uA	120	-	-	V
Forward drop voltage <sup>2)</sup>	V <sub>F</sub>	I <sub>F</sub> =100mA		1.0	V	
	VF	I <sub>F</sub> =200mA	-	-	1.25	V
Reverse leakage current <sup>3)</sup>	1	V <sub>R</sub> =100V	-	-	100	nA
	I <sub>R</sub>	V <sub>R</sub> =100V, Ta=150°C -	-	-	100	uA
Total capacitance	CT	V <sub>R</sub> =0V, f=1MHz	-	-	5	pF
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> =I <sub>R</sub> =30mA, I <sub>rr</sub> =3mA, R <sub>L</sub> =100Ω	-	-	50	ns

<sup>2)</sup> Pulse test:  $t_P \le 380 \mu$ s, Duty cycle  $\le 2\%$ 

<sup>3)</sup> Pulse test:  $t_P \le 5$ ms, Duty cycle  $\le 2\%$ 

#### **Rating and Characteristic Curves**

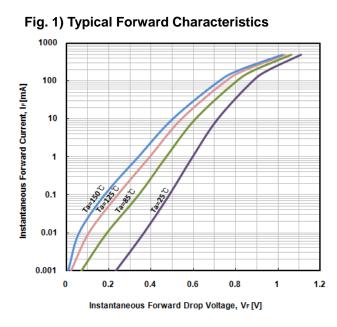


Fig. 2) Typical Reverse Characteristics

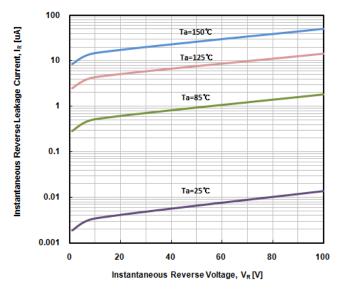
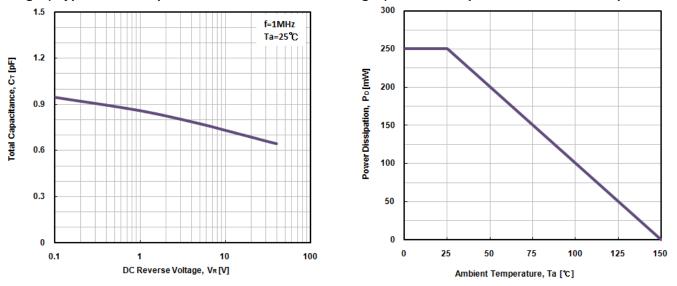
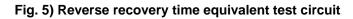
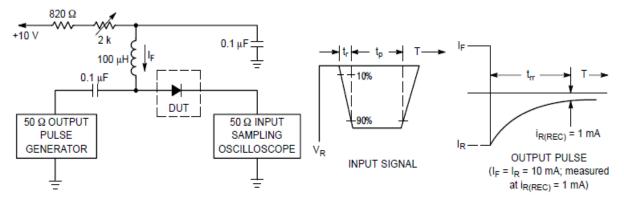


Fig. 4) Power Dissipation vs. Ambient Temperature

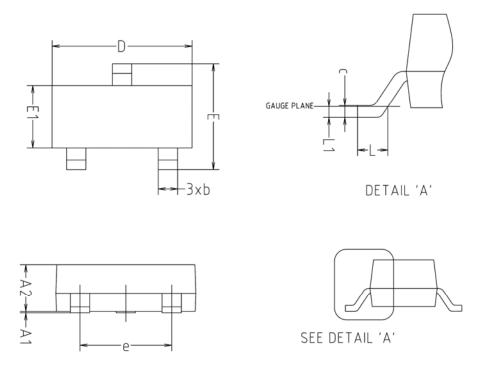
Fig. 3) Typical Total Capacitance Characteristics





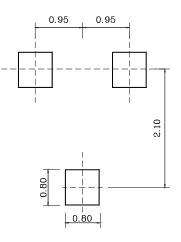


### Package Outline Dimensions



SYMBOL	MILLIMETERS			NOTE
STRIDUL	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	-	0.10	
A2	0.82	-	1.02	
b	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
e	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

#### **%** Recommend PCB solder land (Unit : mm)



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