

High Voltage Switching Diode

General Description

Dual general-purpose switching diodes, fabricated in planar technology, and packaged in small SOT-23 surface mounted device (SMD) packages.

Features and Benefits

- Silicon epitaxial planar diode
- High switching speed
- · Low forward drop voltage and low leakage current
- "Green" device and RoHS compliant device
- Available in full lead (Pb)-free device

Applications

• Ultra high speed switching application

Ordering Information

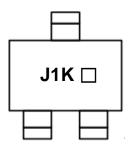






Part Number	Marking Code	Package	Packaging
SDS19WK	J1K 🗆	SOT-23	Tape & Reel

Marking Information



J1K = Specific Device Code

□ = Year & Week Code Marking

Pinning Information

Pin	Description	Simplified Outline	Graphic Symbol
1	Anode (Diode 1)	3	<u>_</u>
2	Anode (Diode 2)		× ×
3	Common Cathode	1 🗄 🗄 2	

Absolute Maximum Ratings (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Maximum repetitive peak reverse voltage	V _{RM}	120	V
Continuous reverse voltage	V _R	100	V
Maximum average forward rectified current	Ι _ο	200	mA
Maximum repetitive peak forward current	I _{FM}	400	mA
Non-repetitive peak forward surge current(t=10ms)	I _{FSM}	1.7	А
Power dissipation ¹⁾	P _D	250	mW

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Thermal Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Thermal resistance, junction to ambient ¹⁾	R _{th(j-a)}	500	°C/W
Operating junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Electrical Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Reverse breakdown voltage	V_{BR}	I _F =100uA	120	-	-	V
Forward drop voltage ²⁾	V _F	I _F =100mA		1.0	V	
	VF	I _F =200mA	-	-	1.25	V
Reverse leakage current ³⁾	1	V _R =100V	-	-	100	nA
	I _R	V _R =100V, Ta=150°C -	-	-	100	uA
Total capacitance	CT	V _R =0V, f=1MHz	-	-	5	pF
Reverse recovery time	t _{rr}	I _F =I _R =30mA, I _{rr} =3mA, R _L =100Ω	-	-	50	ns

²⁾ Pulse test: $t_P \le 380 \mu$ s, Duty cycle $\le 2\%$

³⁾ Pulse test: $t_P \le 5$ ms, Duty cycle $\le 2\%$

Rating and Characteristic Curves

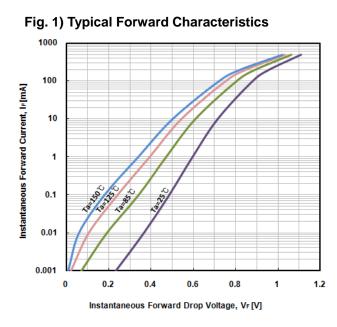


Fig. 2) Typical Reverse Characteristics

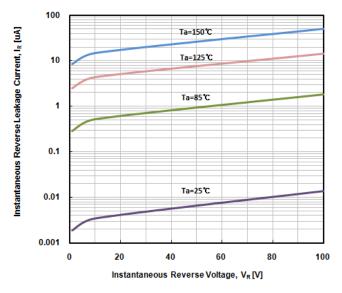
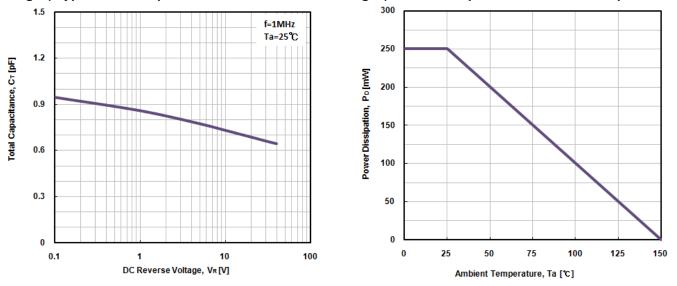
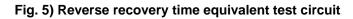
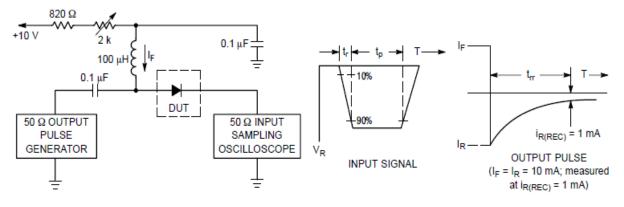


Fig. 4) Power Dissipation vs. Ambient Temperature

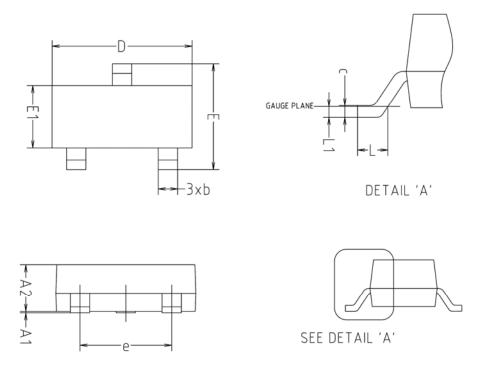
Fig. 3) Typical Total Capacitance Characteristics





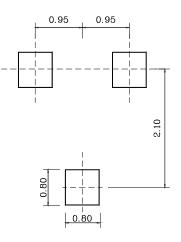


Package Outline Dimensions



SYMBOL	MILLIMETERS			NOTE
STRIDUL	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	-	0.10	
A2	0.82	-	1.02	
b	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
e	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

% Recommend PCB solder land (Unit : mm)



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